

**REMARKS**

Early consideration and allowance of the above-referenced patent application is respectfully requested.

Attached hereto is a marked-up version of the changes made by the current amendment. The attached page is captioned "**Version with markings to show changes made.**"

Respectfully submitted,

Date: Feb. 28, 2002



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**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**In the Specification:**

Lines 20-23 at page 4 have been amended as follows:

elements arranged on a substrate, and [a main current electrode which is arranged vertically to a surface of the substrate and apart from a neighborhood of said plurality of semiconductor elements, wherein] a main current electrode which is arranged near said plurality of semiconductor elements and vertically apart from the surface of the substrate, wherein each of the

**In the claims:**

Claim1 has been amended as follows:

1. (Amended) A semiconductor device, comprising:

a plurality of semiconductor elements arranged on a substrate; and

[a main current electrode which is arranged vertically to a surface of the substrate and apart from a neighborhood of said plurality of semiconductor elements] a main current electrode which is arranged near said plurality of semiconductor elements and vertically apart from the surface of the substrate, wherein

each of said plurality of semiconductor elements and said main current electrode are electrically connected.